

coreboot - Bug #94

cbmem time stamps incorrectly displayed on Asus KGPE-D16

01/31/2017 06:18 PM - Paul Menzel

Status:	New	Start date:	01/31/2017																																																
Priority:	Normal	Due date:																																																	
Assignee:		% Done:	0%																																																
Category:		Estimated time:	0.00 hour																																																
Target version:																																																			
Description																																																			
Manually stopping the start time, and looking at the output of the Dediprog EM100Pro, the start with serial console and 128 GB of RAM takes more than 60 seconds, showing that the times calculated by cbmem -c are off.																																																			
23 entries total:																																																			
<table><tr><td>0:1st timestamp</td><td>21,472</td></tr><tr><td>1:start of rom stage</td><td>21,840 (368)</td></tr><tr><td>2:before ram initialization</td><td>690,904 (669,063)</td></tr><tr><td>3:after ram initialization</td><td>35,440,511 (34,749,606)</td></tr><tr><td>4:end of romstage</td><td>35,532,239 (91,728)</td></tr><tr><td>8:starting to load ramstage</td><td>35,533,829 (1,589)</td></tr><tr><td>15:starting LZMA decompress (ignore for x86)</td><td>35,534,268 (438)</td></tr><tr><td>16:finished LZMA decompress (ignore for x86)</td><td>35,565,001 (30,733)</td></tr><tr><td>9:finished loading ramstage</td><td>35,565,417 (415)</td></tr><tr><td>10:start of ramstage</td><td>35,581,186 (15,769)</td></tr><tr><td>30:device enumeration</td><td>35,585,371 (4,184)</td></tr><tr><td>40:device configuration</td><td>37,881,429 (2,296,058)</td></tr><tr><td>50:device enable</td><td>41,177,247 (3,295,818)</td></tr><tr><td>60:device initialization</td><td>41,359,527 (182,279)</td></tr><tr><td>70:device setup done</td><td>44,731,858 (3,372,331)</td></tr><tr><td>75:cbmem post</td><td>44,735,401 (3,542)</td></tr><tr><td>80:write tables</td><td>44,739,006 (3,604)</td></tr><tr><td>85:finalize chips</td><td>46,363,606 (1,624,600)</td></tr><tr><td>90:load payload</td><td>46,733,933 (370,326)</td></tr><tr><td>15:starting LZMA decompress (ignore for x86)</td><td>46,784,843 (50,910)</td></tr><tr><td>16:finished LZMA decompress (ignore for x86)</td><td>46,797,833 (12,989)</td></tr><tr><td>15:starting LZMA decompress (ignore for x86)</td><td>46,833,433 (35,600)</td></tr><tr><td>16:finished LZMA decompress (ignore for x86)</td><td>46,955,311 (121,877)</td></tr><tr><td>99:selfboot jump</td><td>46,973,104 (17,793)</td></tr></table>				0:1st timestamp	21,472	1:start of rom stage	21,840 (368)	2:before ram initialization	690,904 (669,063)	3:after ram initialization	35,440,511 (34,749,606)	4:end of romstage	35,532,239 (91,728)	8:starting to load ramstage	35,533,829 (1,589)	15:starting LZMA decompress (ignore for x86)	35,534,268 (438)	16:finished LZMA decompress (ignore for x86)	35,565,001 (30,733)	9:finished loading ramstage	35,565,417 (415)	10:start of ramstage	35,581,186 (15,769)	30:device enumeration	35,585,371 (4,184)	40:device configuration	37,881,429 (2,296,058)	50:device enable	41,177,247 (3,295,818)	60:device initialization	41,359,527 (182,279)	70:device setup done	44,731,858 (3,372,331)	75:cbmem post	44,735,401 (3,542)	80:write tables	44,739,006 (3,604)	85:finalize chips	46,363,606 (1,624,600)	90:load payload	46,733,933 (370,326)	15:starting LZMA decompress (ignore for x86)	46,784,843 (50,910)	16:finished LZMA decompress (ignore for x86)	46,797,833 (12,989)	15:starting LZMA decompress (ignore for x86)	46,833,433 (35,600)	16:finished LZMA decompress (ignore for x86)	46,955,311 (121,877)	99:selfboot jump	46,973,104 (17,793)
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Total Time: 46,951,620																																																			
Nico suggested, that cbmem takes some value from the running system to scale them, which seems to be wrong.																																																			