

coreboot - Bug #166

UEFI MP Services

05/09/2018 06:36 PM - Aaron Durbin

Status:	New	Start date:	05/09/2018
Priority:	Normal	Due date:	
Assignee:		% Done:	0%
Category:		Estimated time:	0.00 hour
Target version:			
Description			
Bug in question on Chrome OS side: https://issuetracker.google.com/74436746 -- Yes, I know this is only viewable by Google partners. Filed March 9th.			
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While meeting w/ intel folks they really want to do MP init in FSP. I (and others) really don't want to do that. What was floated was having a UEFI MP services module that FSP could utilize. I think this is doable, and we could theoretically utilize this for mp_init currently. Food for thought. Just filing this so we can track it.			
Beyond that there's discussion how some patch may have broken WIP on CNL platform. From a technical standpoint the discussion arose from the initial patch implementation submitted. That's pretty much the extent.			

History

#1 - 05/09/2018 06:38 PM - Aaron Durbin

When I wrote 'for mp_init currently' I meant use mp_init as the implementation for the UEFI PPI callbacks. Just wanted to clarify there.

#2 - 06/01/2018 04:05 PM - Idwer Vollerling

Q: why is it a good idea to perform MP init once coreboot/FSP/(firmware) is able to load extensible binaries?