

## coreboot - Bug #130

kernel: pci 0000:00:1e.0: bridge has subordinate 06 but max busn 09

07/17/2017 01:12 PM - Paul Menzel

<b>Status:</b>	New	<b>Start date:</b>	07/17/2017
<b>Priority:</b>	Normal	<b>Due date:</b>	
<b>Assignee:</b>		<b>% Done:</b>	0%
<b>Category:</b>		<b>Estimated time:</b>	0.00 hour
<b>Target version:</b>			
<b>Description</b>			
On some Intel devices, Linux logs the message below.			
kernel: pci 0000:00:1e.0: bridge has subordinate 06 but max busn 09			
Running git grep "bridge has subordinate", shows four devices.			
<ol style="list-style-type: none"><li>1. getac/p470</li><li>2. lenovo/t500</li><li>3. lenovo/t60</li><li>4. lenovo/x60</li></ol>			
kmallki commented as below in <a href="#">#coreboot@irc.freenode.net</a> .			
PCI: pci_scan_bus for bus 05 PCI: 05:00.0 [1180/0476] enabled PCI: 05:00.1 [1180/0552] enabled PCI: 05:00.2 [1180/0822] enabled PCI: 05:00.3 [1180/0843] enabled PCI: pci_scan_bus for bus 06 << *** secondary of 05:00.0 *** scan_bus: scanning of bus PCI: 05:00.0 took 55 usecs scan_bus: scanning of bus PCI: 00:1e.0 took 145 usecs  ... ...  Ricoh RL5c476: Initializing. CF Base = 0 CF boot not enabled. PCI: 05:00.0 init finished in 4 usecs << *** too late ? *** PCI: 05:00.1 init ... PCI: 05:00.1 init finished in 1 usecs PCI: 05:00.2 init ... PCI: 05:00.2 init finished in 1 usecs PCI: 05:00.3 init ... PCI: 05:00.3 init finished in 1 usecs			

## History

### #1 - 07/18/2017 10:18 AM - Kyösti Mälkki

Device 5:0.0 is cardbus controller with hotplug capability. We should leave some reserve of free PCI bus numbers in case the user plugs in a PCI bridge at runtime. I am not sure where max busn 9 comes from in the kernel messages.