Issues

#	Project	Tracker	Status	Priority	Category	Subject	Assignee	Updated
262	coreboot	Bug	Resolved	Normal		Sandy Bridge/IvyBridge: Only advertise supported MWAIT states as ACPI C states		12/20/2023 01:49 PM
201	coreboot	Feature	Resolved	Normal		Implement free() in ramstage		01/21/2020 10:56 AM
197	coreboot	Feature	Resolved	Normal		QEMU x86_64 support		12/20/2023 01:50 PM
188	coreboot	Feature	Resolved	Normal		QEMU aarch64 support		01/21/2020 03:00 PM
185	coreboot	Feature	Resolved	Normal		x86_64 support on real hardware		12/20/2023 01:50 PM
44	coreboot	Feature	Closed	Normal		Get rid of arch/x86/romcc_console.c	Kyösti Mälkki	08/21/2017 11:58 PM
22	coreboot	Bug	Resolved	Normal		X230 native raminit failed	Patrick Rudolph	02/11/2022 07:16 PM