

## Issues

#	Project	Tracker	Status	Priority	Category	Subject	Assignee	Updated
218	coreboot	Feature	Resolved	Normal	chipset configuration	Setting Max Payload Size of End Point in correspondence with Max Payload Size of Root Complex	Kyösti Mälkki	12/19/2019 05:05 PM
126	coreboot	Bug	Resolved	Normal		*AGESA: Fix UMA calculations* breaks boot on ASRock E350M1	Kyösti Mälkki	07/18/2017 09:47 AM
82	coreboot	Bug	Resolved	Normal		CAR fails on LGA775 pentium 4-like CPUs	Kyösti Mälkki	12/14/2016 01:33 PM
45	coreboot	Bug	Closed	Normal		F2A85-M fails to detect 24-32GB of RAM properly	Kyösti Mälkki	02/11/2022 07:13 PM
44	coreboot	Feature	Closed	Normal		Get rid of arch/x86/romcc_console.c	Kyösti Mälkki	08/21/2017 11:58 PM